PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants   :   Michael R. Buckmaster et al.

Filed   :   July 25, 2001

For   :   SYSTEM AND METHOD FOR TESTING AN
                    EMBEDDED MICROPROCESSOR SYSTEM
                    CONTAINING PHYSICAL AND/OR SIMULATED
                    HARDWARE

                          Docket No.   :   120073.415D1
                          Date   :   July 25, 2001

Box Patent Application
Commissioner for Patents
Washington, DC 20231

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents:

In accordance with 37 C.F.R. §§ 1.56 and 1.97 through 1.98, applicants wish to make known to the Patent and Trademark Office the three references set forth on the attached Form PTO-1449. This application relies, under 35 U.S.C. § 120, on the earlier filing date of prior Application No. 09/024,324, filed February 17, 1998. The references listed on the attached Form PTO-1449 were submitted to and/or cited by the Patent and Trademark Office in this prior application and, therefore, are not required to be provided in this application. If the Examiner wishes, copies will be provided upon request. As to any reference supplied, applicants do not admit that it is "prior art" under 35 U.S.C. §§ 102 or 103, and specifically reserve the right to traverse or antedate any such reference, as by a showing under 37 C.F.R. § 1.131 or other method. Although the aforesaid references are made known to the Patent and Trademark Office in compliance with applicants' duty to disclose all information they are aware of which is believed relevant to the examination of the above-identified application, applicants believe that their invention is patentable. Applicants list the Berger, et al. reference because it was cited by the Examiner in the parent application. Please note, however, that Applicants submitted a Declaration under 37 C.F.R. § 1.131 swearing behind the Berger, et al. reference. The

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Declaration is in the parent file. Applicants will provide the Examiner with a copy of the Declaration if desired.

Please acknowledge receipt of this Information Disclosure Statement and kindly make the cited references of record in the above-identified application.

Respectfully submitted,

Michael R. Buckmaster et al.

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FORM PTO-1449 (REV.7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 120073.415D1	APPLICATION NO.
INFORMATION DISCLOSURE STATEMENT *(Use several sheets if necessary)*		APPLICANTS Michael R. Buckmaster et al.	
		FILING DATE July 25, 2001	GROUP ART UNIT

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
	AA						
	AB						
	AC						
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						
	AJ						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	TRANSLATION YES	NO
	AK					
	AL					

OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

	AM	Manku et al., "Circuit Partitioning with Partial Order for Mixed Simulation Emulation Environment," Sixth IEEE International Workshop on Rapid System Prototyping, pp. 201-207. June 1995.
	AN	Borgatti et al., "A Smoothly Upgradable Approach to Virtual Emulation of HW/SW Systems," Proceedings of the Seventh International Workshop on Rapid System Prototyping, pp. 83-88. June 1996.
	AO	Bauer et al., "Hardware/Software Co-Simulation in a VHDL-based Test Bench Approach," IEEE Proceedings of the 34th Design Automation Conference, pp. 774-779. June 1996.
	AP	Berger et al., "Co-Verification Handles More Complex Embedded Systems," Electronic Design, pp. 96-104. September 15, 1997.

EXAMINER	DATE CONSIDERED

* EXAMINER: Initial if reference considered, whether or not criteria is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant(s).

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